



# REVISION HISTORY

| VER. | DATA       | DESCRIPTION OF CHANGES   |
|------|------------|--|
| 0.1  | 2023-12-20 | Fixed PCB Footprint (Changed size)   |
| 0.2  | 2023-01-08 | eMMC (U7) : Add KLM4G1FETE-B041001<br>CPU (U6) : AM6232ATGGHAALW/TI -> AM6232ATCGHAALW/TI<br>MEMORY (DDR RAM) (U9) : MT40A1G16TB-062E/MICRON -> MT40A1G16TB-062E<br>R60,R63,R65 : SIZE 0603->1005<br>Delete VDDA_CORE_CSI<br>Delete BD4,C117,C118,C120<br>Delete VDDA_1V8_CSIRX<br>Delete BD1,C107,C108,C109 |



DDR Line Swap

DDR

PrGrp: VDD5\_DDR, VDD5\_DDR\_C

| Pin           | Signal | Signal        | Signal |
|---------------|--------|---------------|--------|
| D0R_D00       | F0     | DDR0_D00      | H0     |
| D0R_D01       | F1     | DDR0_D01      | H1     |
| D0R_D02       | F2     | DDR0_D02      | H2     |
| D0R_D03       | F3     | DDR0_D03      | H3     |
| D0R_D04       | F4     | DDR0_D04      | H4     |
| D0R_D05       | F5     | DDR0_D05      | H5     |
| D0R_D06       | F6     | DDR0_D06      | H6     |
| D0R_D07       | F7     | DDR0_D07      | H7     |
| D0R_D08       | F8     | DDR0_D08      | H8     |
| D0R_D09       | F9     | DDR0_D09      | H9     |
| D0R_D10       | F10    | DDR0_D10      | H10    |
| D0R_D11       | F11    | DDR0_D11      | H11    |
| D0R_D12       | F12    | DDR0_D12      | H12    |
| D0R_D13       | F13    | DDR0_D13      | H13    |
| D0R_D14       | F14    | DDR0_D14      | H14    |
| D0R_D15       | F15    | DDR0_D15      | H15    |
| D0R_BA0       | M1     | DDR0_BA0      | E1     |
| D0R_BA1       | N1     | DDR0_BA1      | E2     |
| D0R_BG0       | T4     | DDR0_BG0      | V1     |
| D0R_BG1       | N2     | DDR0_BG1      | V2     |
| Reserved Pins | T2     | RSVD4         |        |
| DDR_CLKP      | L1     | RSVD5         |        |
| DDR_CKN       | L2     | DDR0_CK0      |        |
| DDR_CKE       | H2     | DDR0_CK0_N    |        |
| DDR_CSn       | J4     | DDR0_CKE1     |        |
| DDR_ODT       | L6     | DDR0_CSn_N    |        |
| DDR_ACTn      | H1     | DDR0_CS0_N    |        |
| DDR_ALERtn    | N6     | DDR0_ODT0     |        |
| R54           | M2     | DDR0_ACT_N    |        |
| D0R_A15_CAS   | M4     | DDR0_ALERtn   |        |
| DDR_PARITY    | T1     | DDR0_ALERT_N  |        |
| DDR_A16_RAS   | M5     | DDR0_CAL0     |        |
| DDR_RESET#    | G1     | DDR0_CAS_N    |        |
| DDR_A14_WE#   | N3     | DDR0_PAR      |        |
|               |        | DDR0_RAS_N    |        |
|               |        | DDR0_RESETO_N |        |
|               |        | DDR0_WE_N     |        |

VDD\_DDR4

R53  
DNI

DDR\_RESET#

R55  
2.2K

AMR232ATCGH4LW

**SOC - MMIO**

**MMC0**

PwrGp:VDDSRV4

**MMC1**

PwrGp:VDDSRV5

**GENERAL**

PwrGp:VDDSRV0

**MMC2**

PwrGp:VDDSRV6

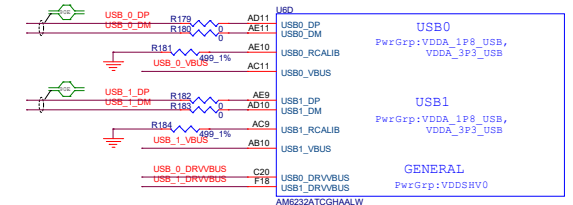
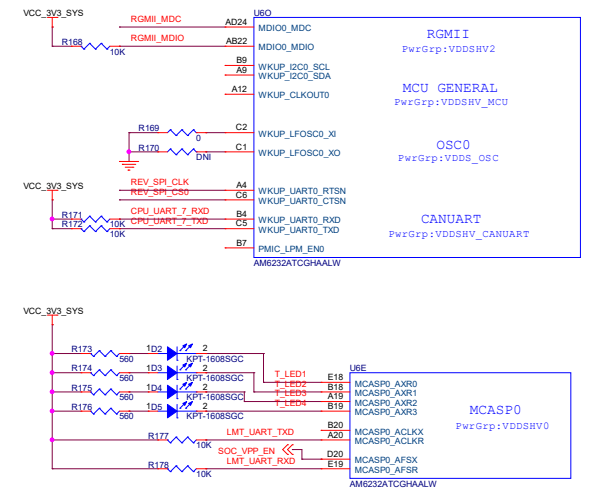
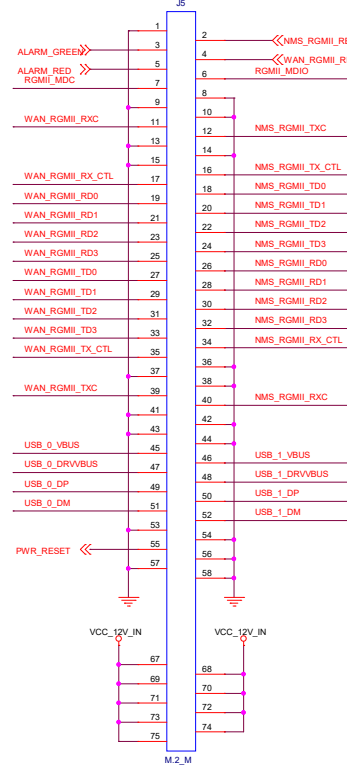
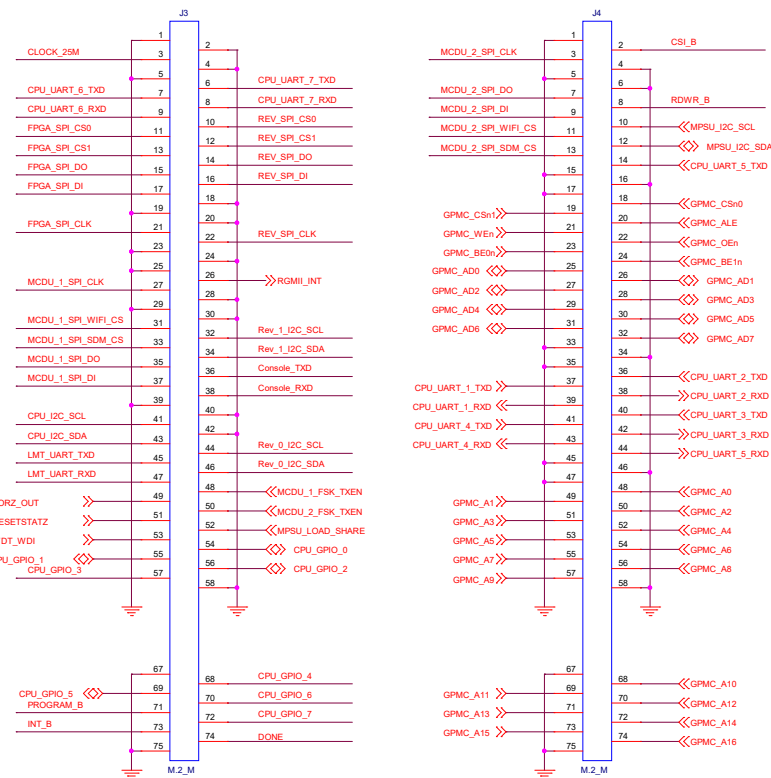
1..8V

MMIO232A2TGH4LW

[illegible][illegible]



## M.2 Connectors



## OSCILLATOR

